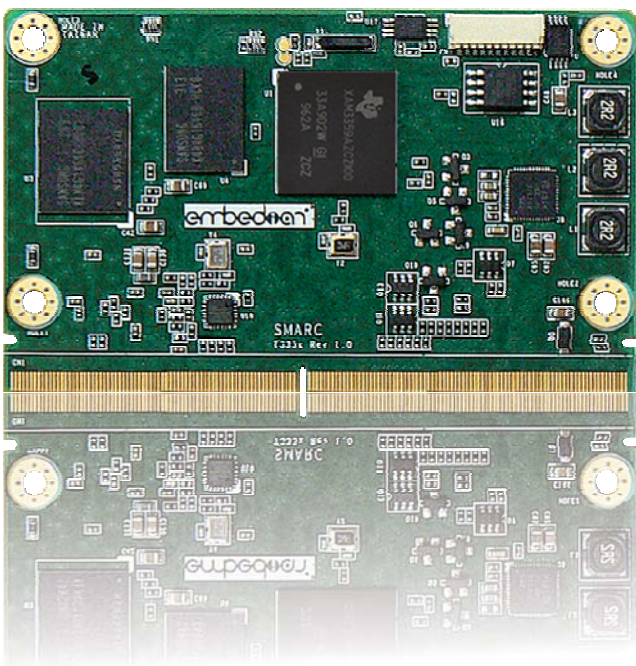


# embedian



A8

## SMARC-T335X



### Reduced cost

Mass production equals a better price performance ratio

### Improved quality

Mass production equals higher product quality

### Improved negotiating power for the buyer

Standards drive product differentiation and competition toward price and service and away from features. This gives buyer both better pricing and better support.

## HIGHLIGHTS

- TI Sitara AM335X ARM Cortex-A8
- 600Mhz, 800Mhz and 1Ghz
- 512MB DDR3L and 8GB eMMC
- 2 x Ethernet interfaces
- Long-term availability (10+ years)
- SMARC Compliant

### SMARC T335X with maximum flexibility

SMARC is the first COM standard to be built specifically for modern ARM-Cortex system on chips (SoCs), aiming to efficiently pass along ARM benefits such as low power consumption to COTS designs. Texas Instrument'AM335x nearly embodies a perfect processor for the SMARC concept.

The benefits of standardization are listed as follows.

#### Standard architectures

Allows software teams to develop new applications faster with fewer people.

#### Scalable and flexible

More module offerings can be applied to the same platform.



modularized  
design

low power

wide

temperature

extensive

supports

cost

effective

high

performance

long

lifecycle

## Technical Information

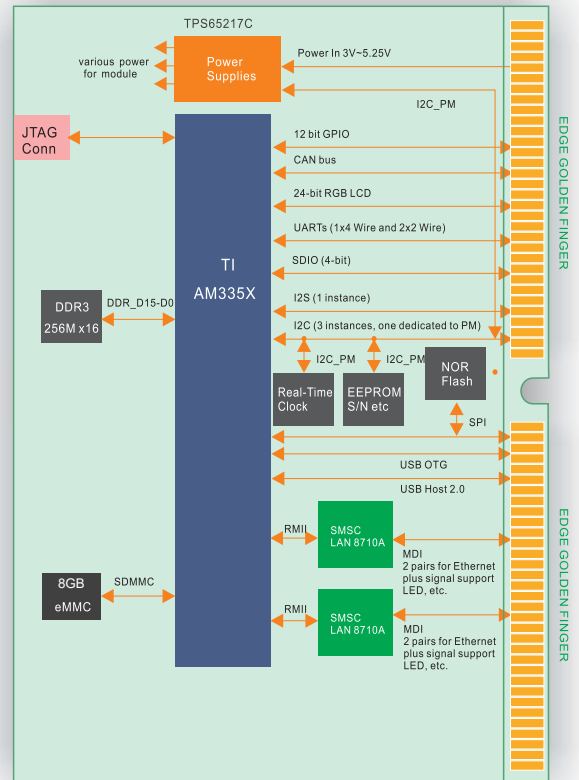
### SMARC T335X Module

<b>Processor</b>	TI Sitara AM335X ARM Cortex-A8 up to 1GHz
<b>Memory</b>	Onboard 4GB eMMC Onboard 512MB DDR3 Onboard 4MB SPI NOR Flash Onboard 4KB EEPROM
<b>Networking</b>	2 x 10/100Mbps Ethernet
<b>Display</b>	Parallel RGB LCD 24-bit
<b>Expansion</b>	1 x SD/SDHC, 1 x USB Host 2.0
<b>USB</b>	1 x USB Host 2.0, 1 x USB OTG
<b>Additional Interfaces</b>	3 x UARTs, 2 x SPIs, 3 x I2C, 1 x I2S, 1 x CAN bus, 2 x PWM, 12 x GPIOs
<b>SW Support</b>	Linux 3.2 / Linux 3.12 Android ICS
<b>Power</b>	2 Watts typical

### Evaluation Carrier

<b>Ethernet</b>	2 (RJ45)
<b>RS 232</b>	2 (10-way 2mm header)
<b>USB Host 2.0</b>	1 (Type A)
<b>USB OTG</b>	1 (mini B)
<b>SD/SDIO Card Slot</b>	1
<b>CAN Bus</b>	1 (10-way 2mm header)
<b>SPI</b>	2 (14-way 2mm header)
<b>I2C</b>	2 (14-way 2mm header)
<b>GPIO</b>	1 (14-way 2mm header)
<b>LVDS</b>	1 (24-bit ZIF)
<b>Parallel LCD</b>	1 (18-bit FPC)
<b>4-wire resistive touch</b>	1 (ZIF)
<b>Stereo Audio</b>	1 (10-way 2.54mm header)

## Block Diagram



## Evaluation Kit – Accelerated Design

The SMARC T335X Evaluation Kit is intended to serve multiple needs and summarized as followed:

- SMARC T335X bring-up platform for hardware and software development.
- Module validation platform.
- Customer evaluation platform.
- Customer design reference.
- Manufacturing test platform.
- Flexible prototyping vehicle (facilitated by multiple mezzanines).

